

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>	ATTORNEY DOCKET NO. 826.1880	APPLICATION NO. (Unassigned)
	FIRST NAMED INVENTOR Takahisa HATAKEYAMA et al.	
	FILING DATE July 9, 2003	GROUP ART UNIT

U.S. PATENT DOCUMENTS

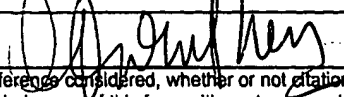
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
OS	AA	2001/0018736	08/30/01	Hashimoto et al.			
	AB	2001/0014157	08/16/01	Hashimoto et al.			
	AC	2003/0050894	03/13/03	Kambayashi et al.			
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		DOCUMENTS NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO	
OS	AG	03083132	04/09/91	Japan			Abstract	
	AH	2001-230770	08/24/01	Japan				X
	AI	2000-260121	09/22/00	Japan				X
	AJ							
	AK							
	AL							

OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

OS	AM	Dallas Semiconductor: DS5002FP Secure Microprocessor Chip, pp. 1-29
	AN	Anderson, "Tamper Resistance - A Cautionary Note", <u>USENIX Association</u> , pp. 1-11 (1996)
	AO	

EXAMINER 	DATE CONSIDERED 7/7/06
<small>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>	